

The diagram illustrates an iris control system for a video camera. Light enters from the left, passing through a lens (501), an iris (503), and an ND filter (502). The light then hits a CCD sensor (504), which outputs a signal to a CDS AGC circuit (505). The output of the CDS AGC circuit (506) is sent to a video signal processing circuit. A feedback loop is formed by a luminance signal detecting circuit (507) and an iris control signal computing circuit (511). The iris control signal computing circuit (511) branches into two modes: an iris high-speed control mode part (512) and an iris low-speed control mode part (513). Both modes feed into an iris control mode selecting circuit (515). This circuit also receives input from an ND-filter switching lever (510) and an ND-filter on/off detection line (514). The output of the iris control mode selecting circuit (515) is sent to a driver (509), which controls the iris (503).

FIG. 2

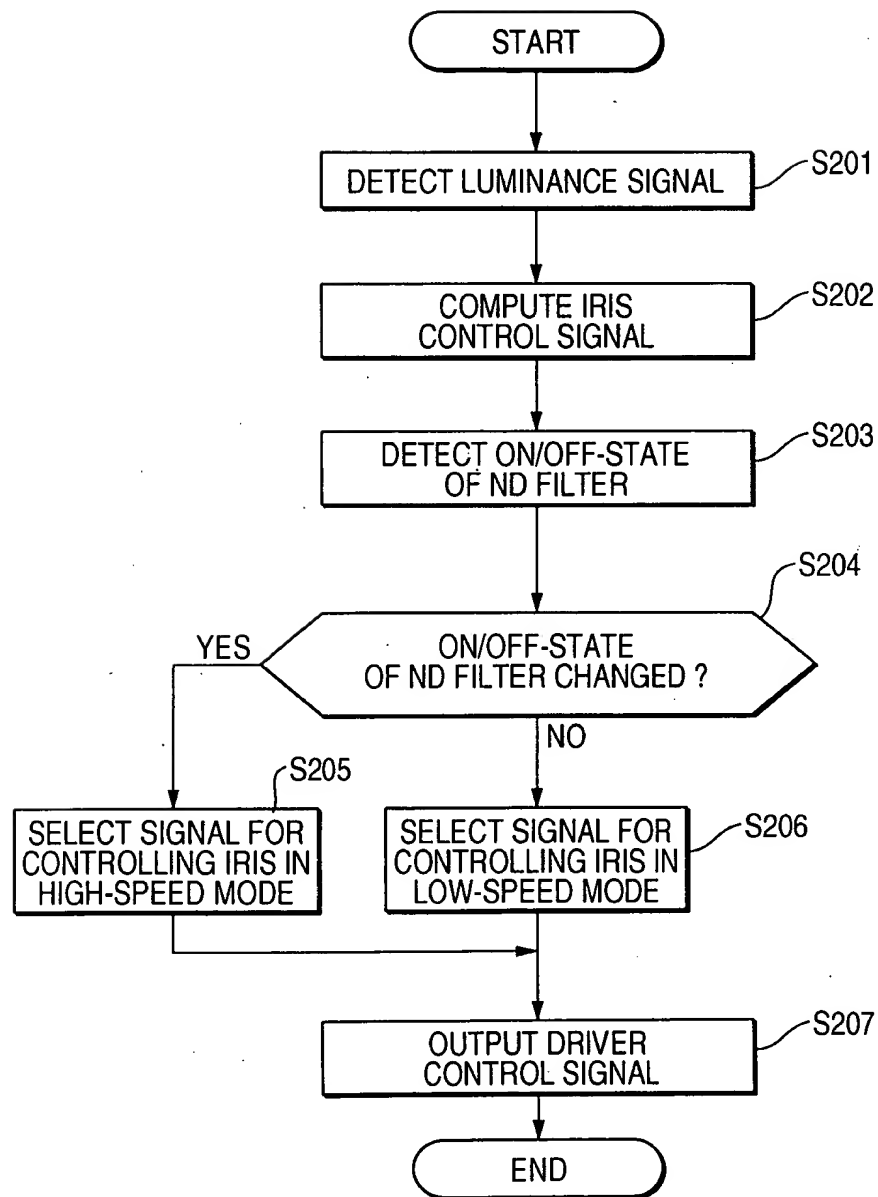


FIG. 3

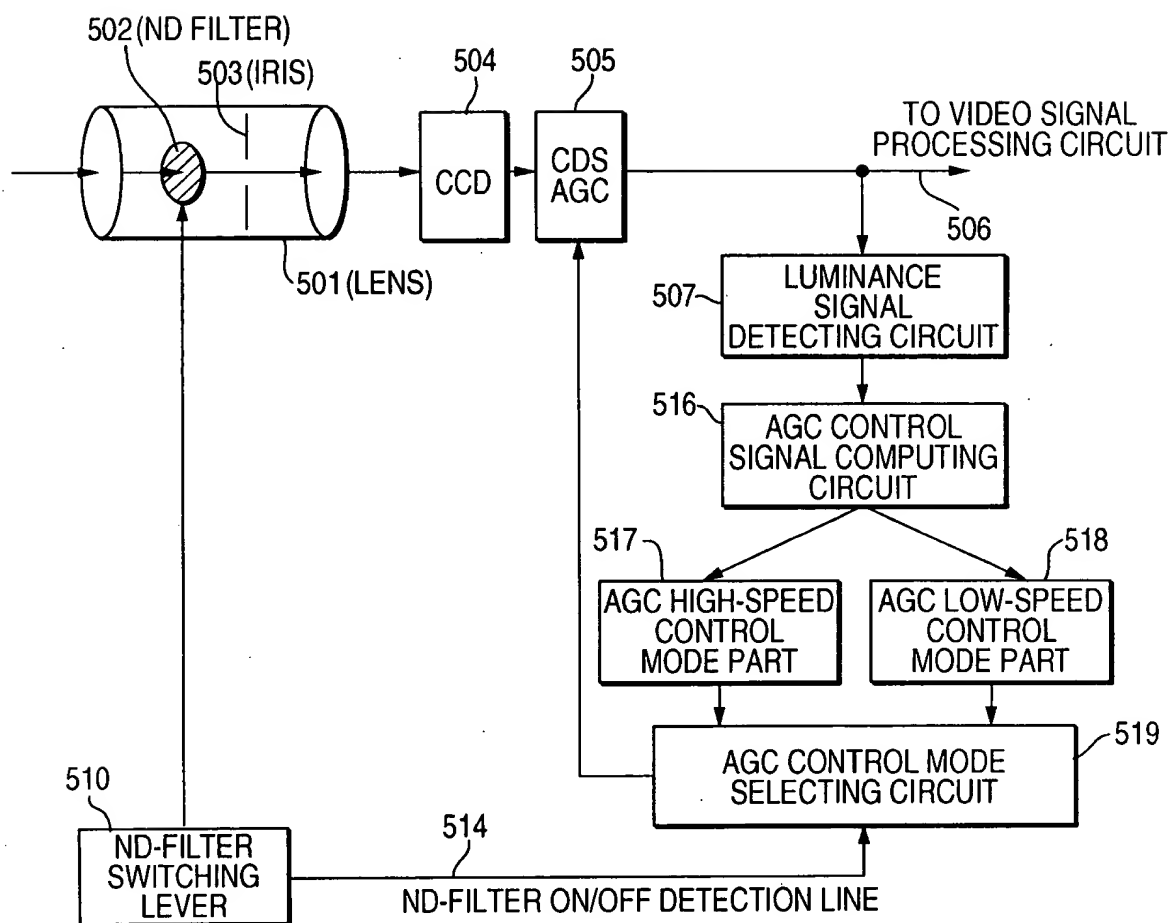


FIG. 4

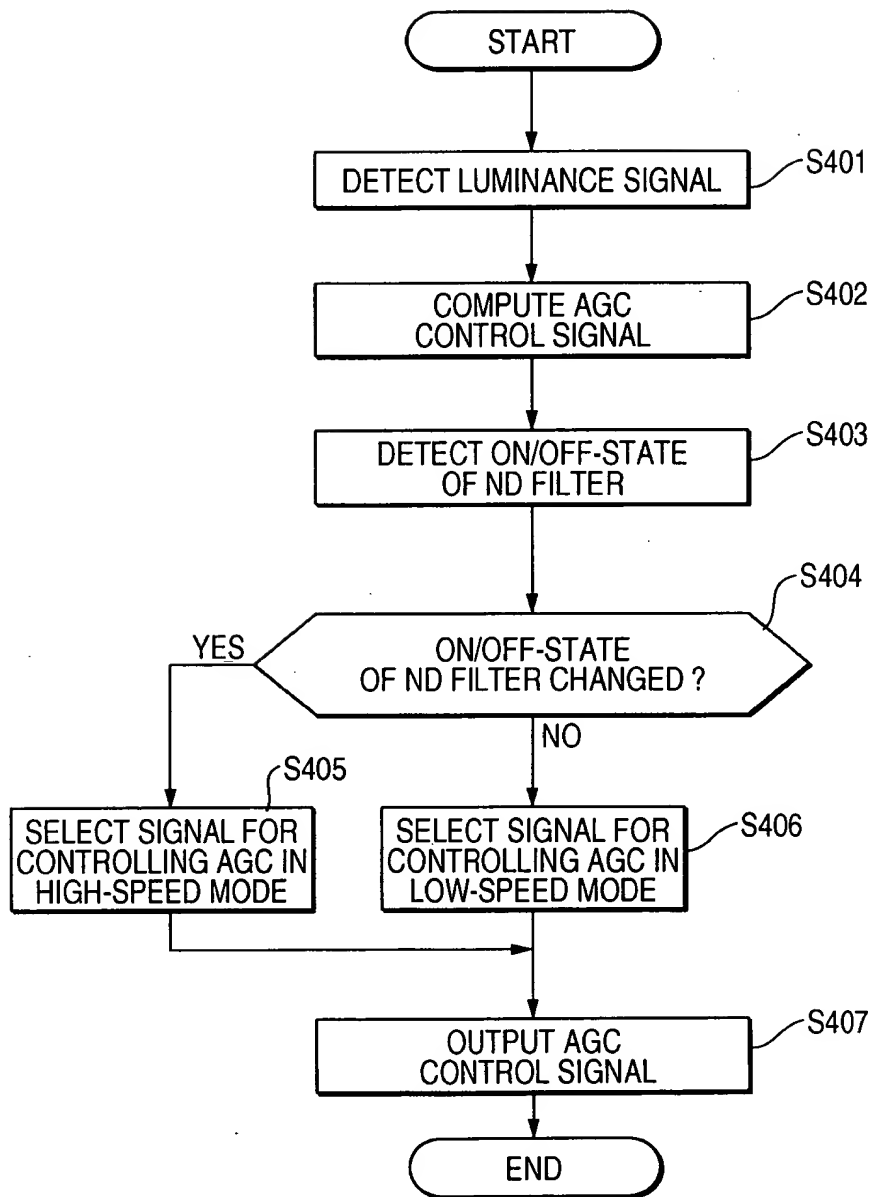


FIG. 5(a)
IN THE CASE OF PRIOR ART

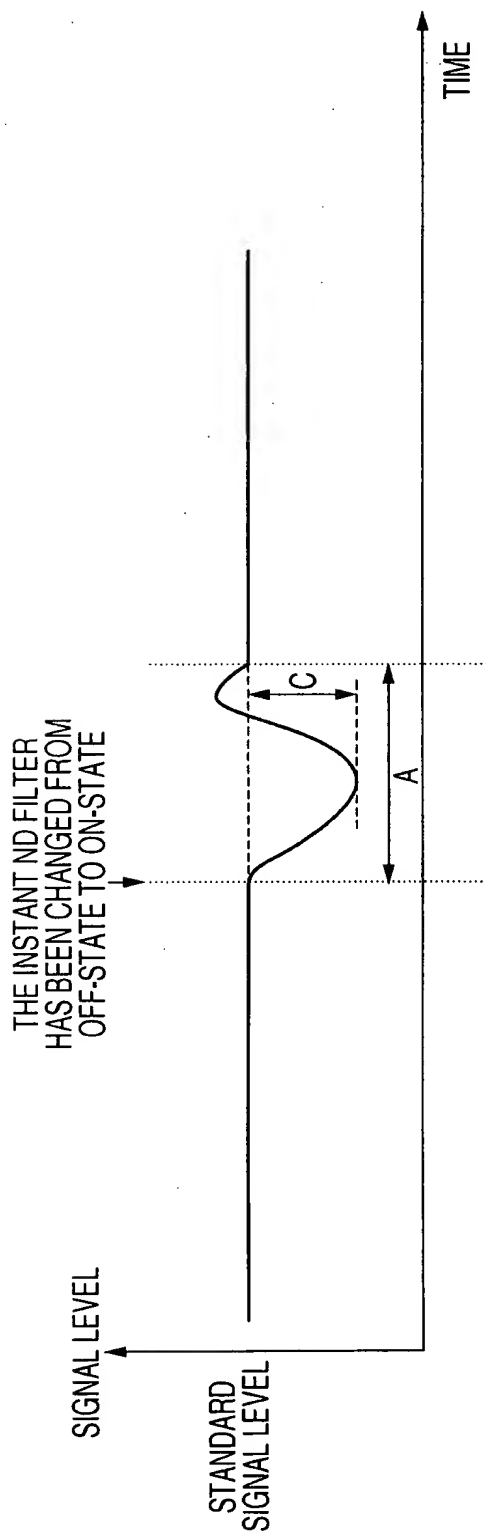


FIG. 5(b)
IN THE CASE OF INVENTION

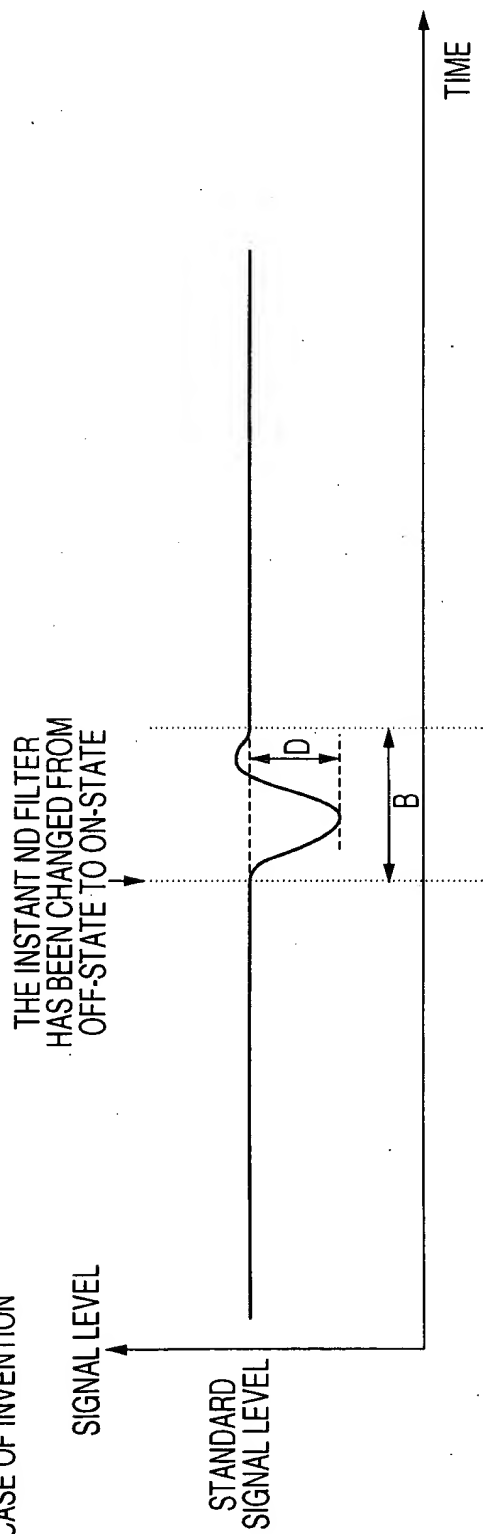
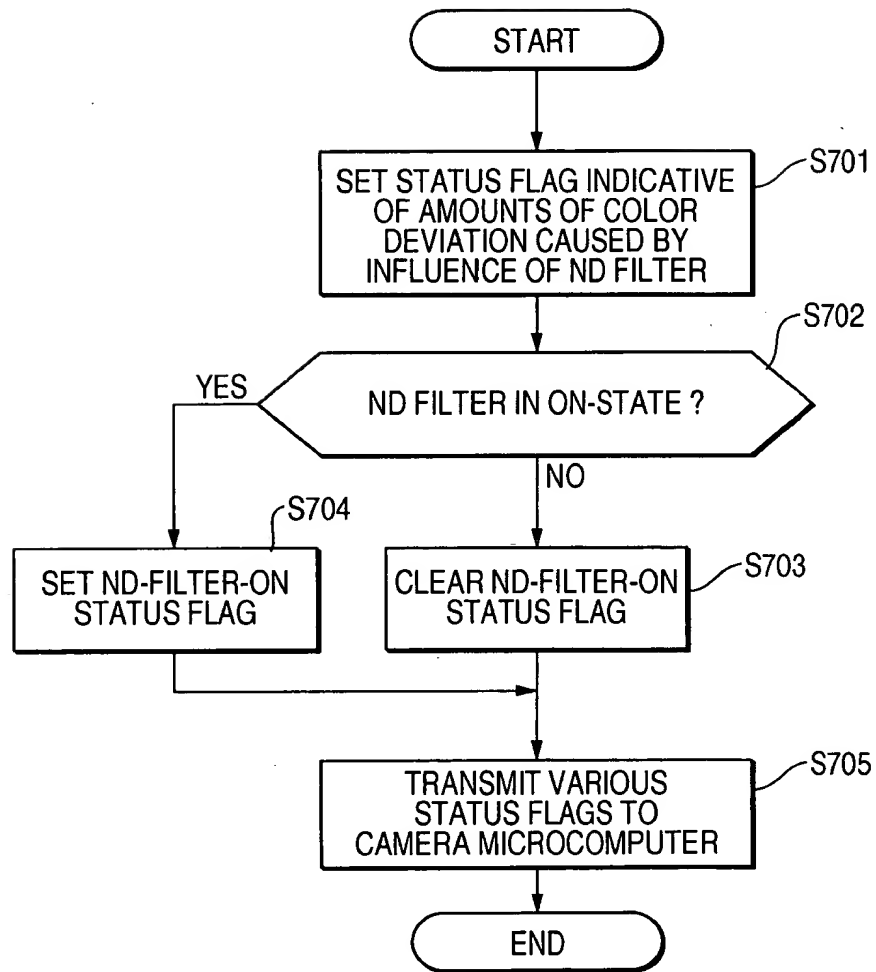


FIG. 6



007720" T42E7960

FIG. 7

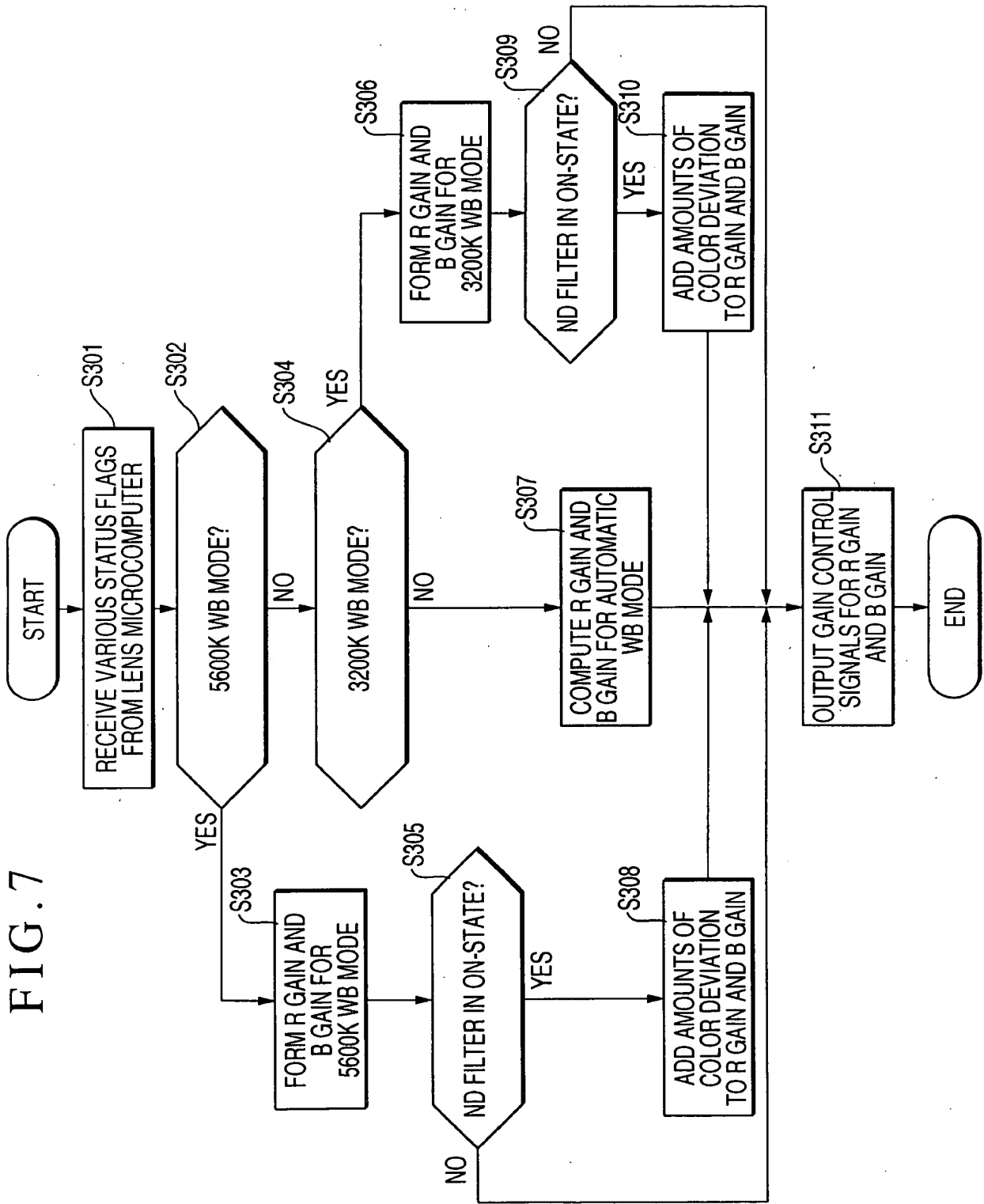


FIG. 8

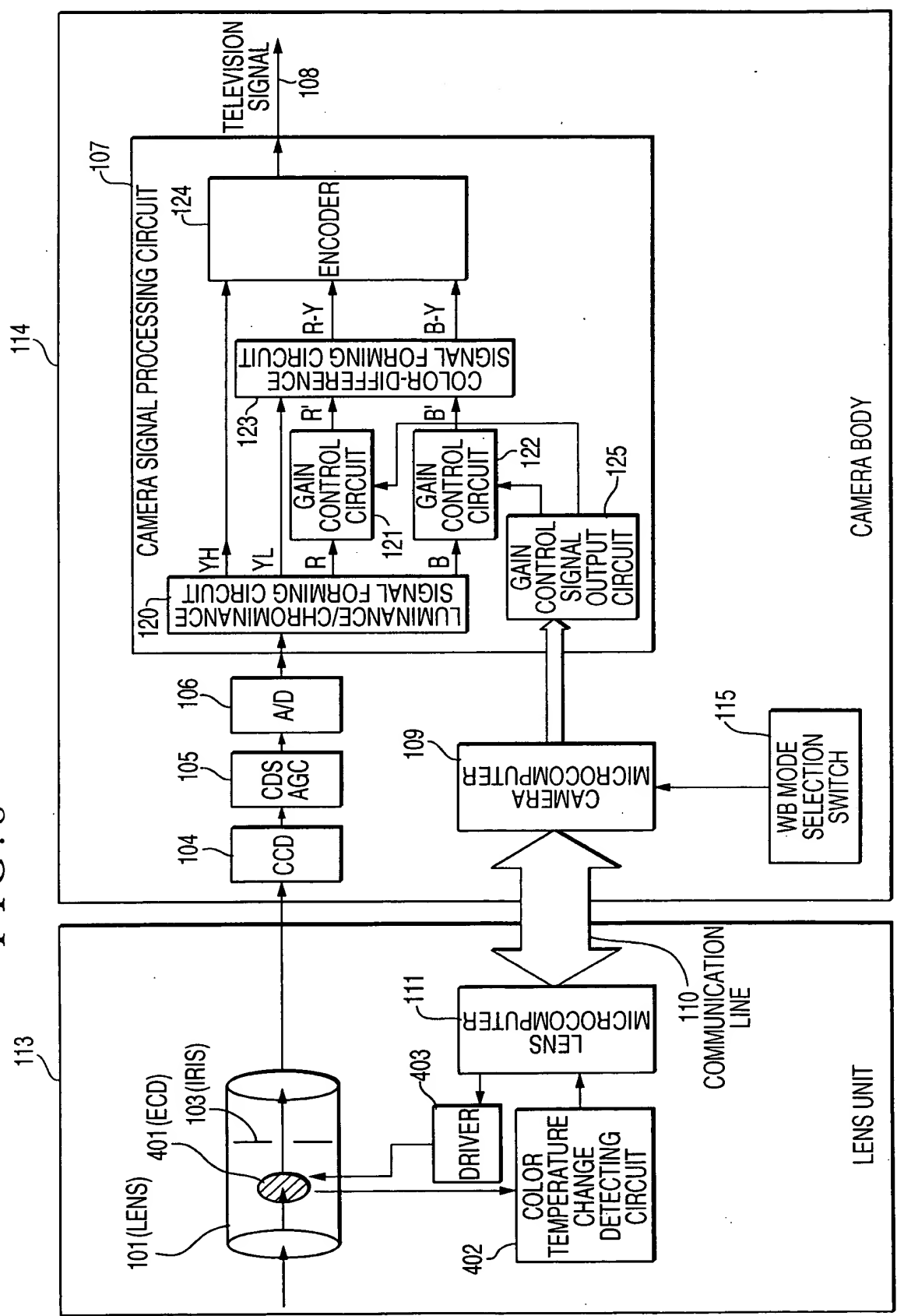


FIG. 9

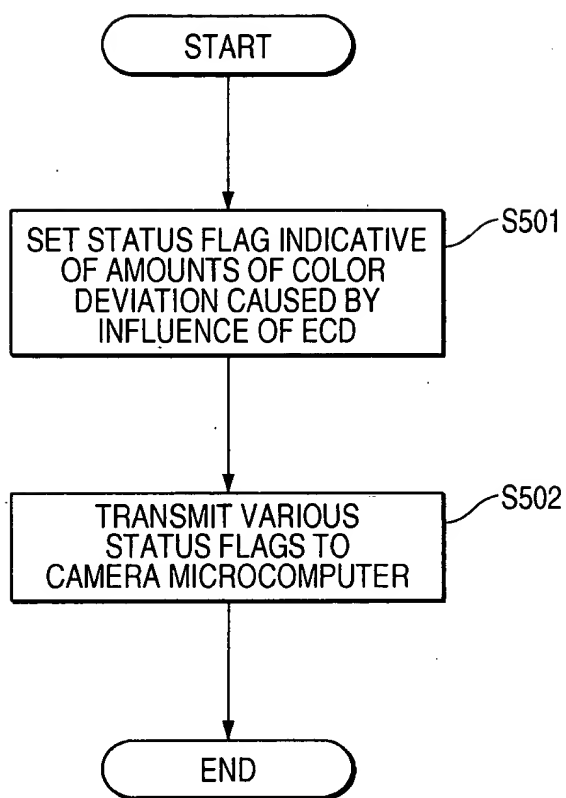


FIG. 10

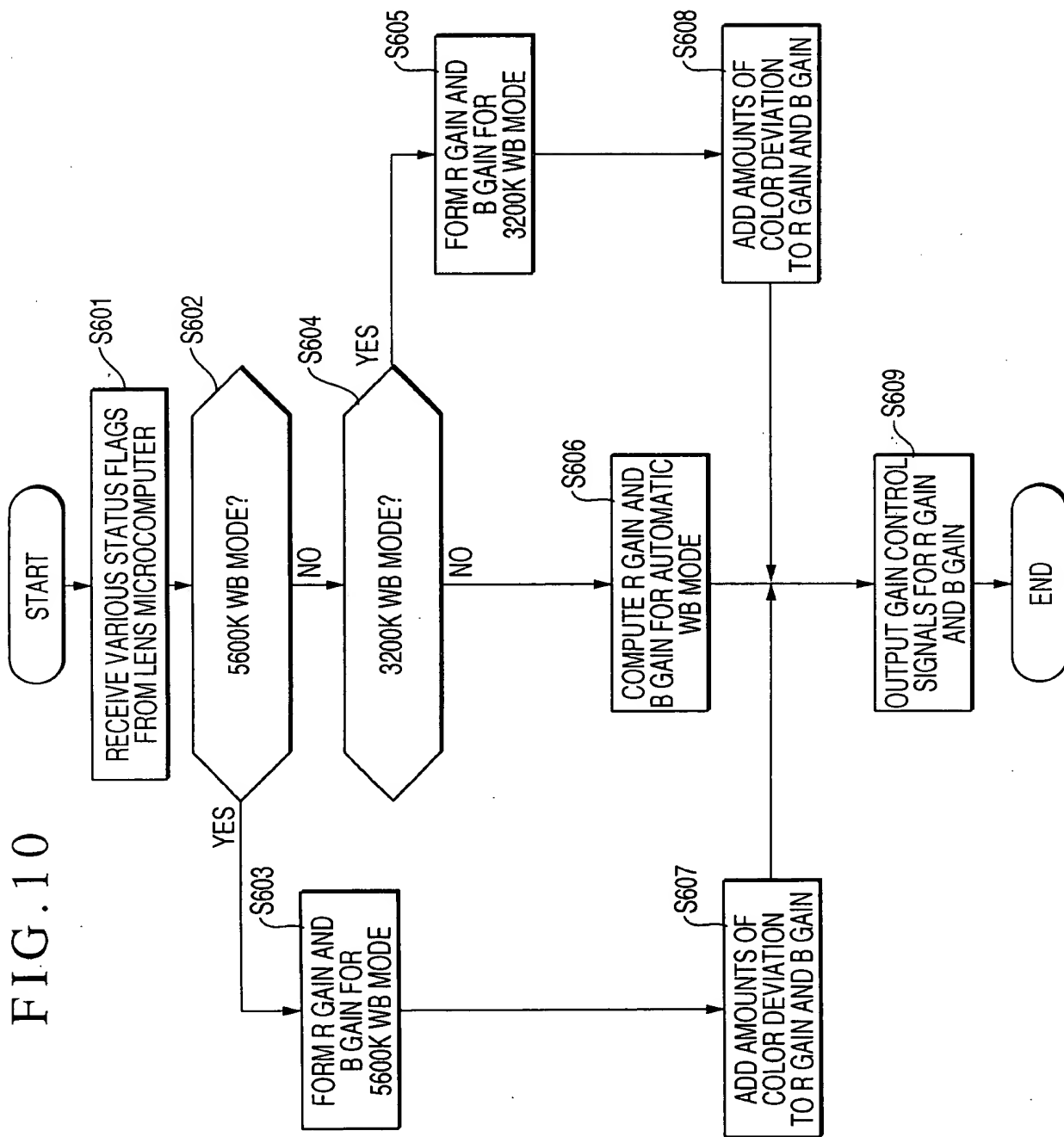


FIG. 11

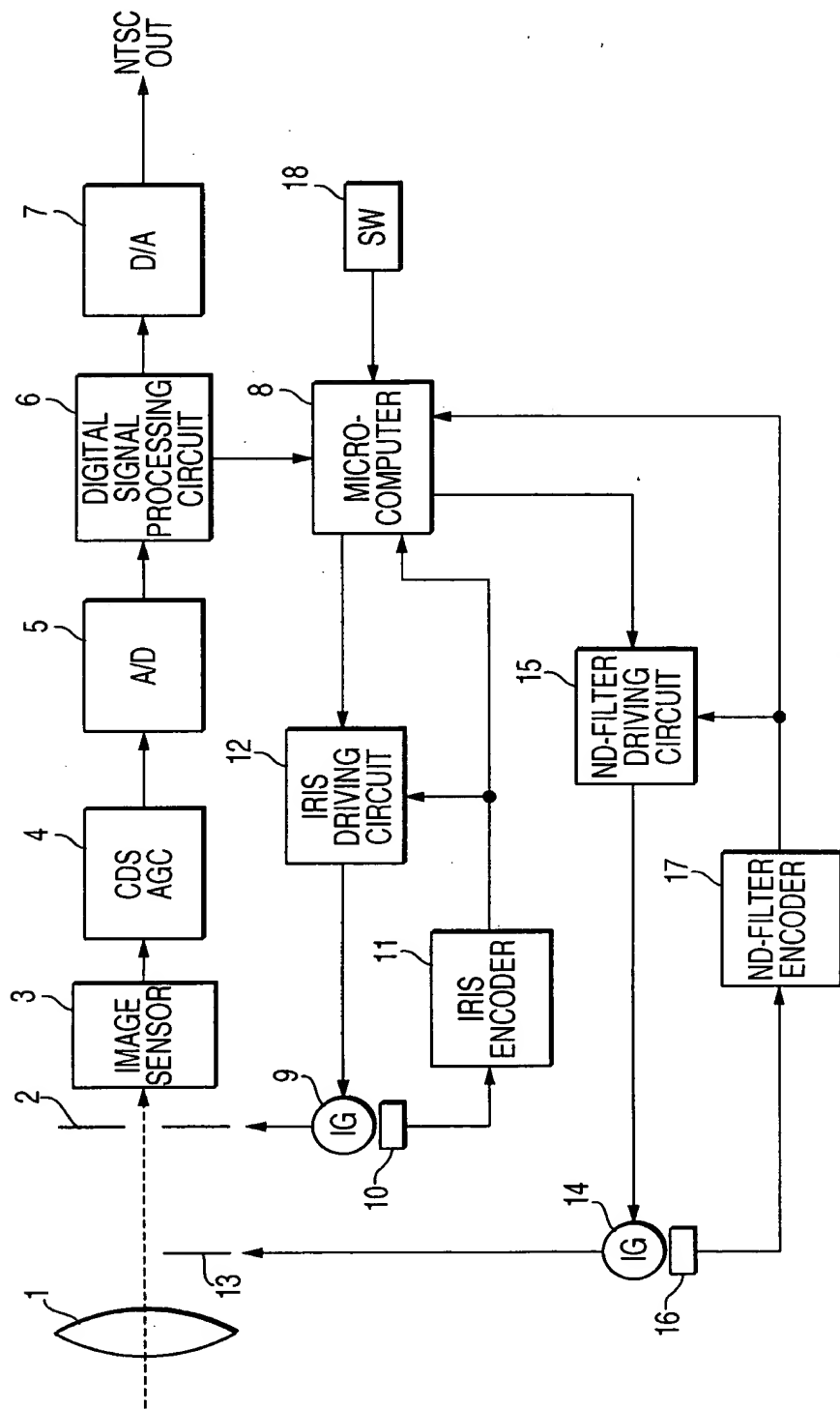


FIG. 12

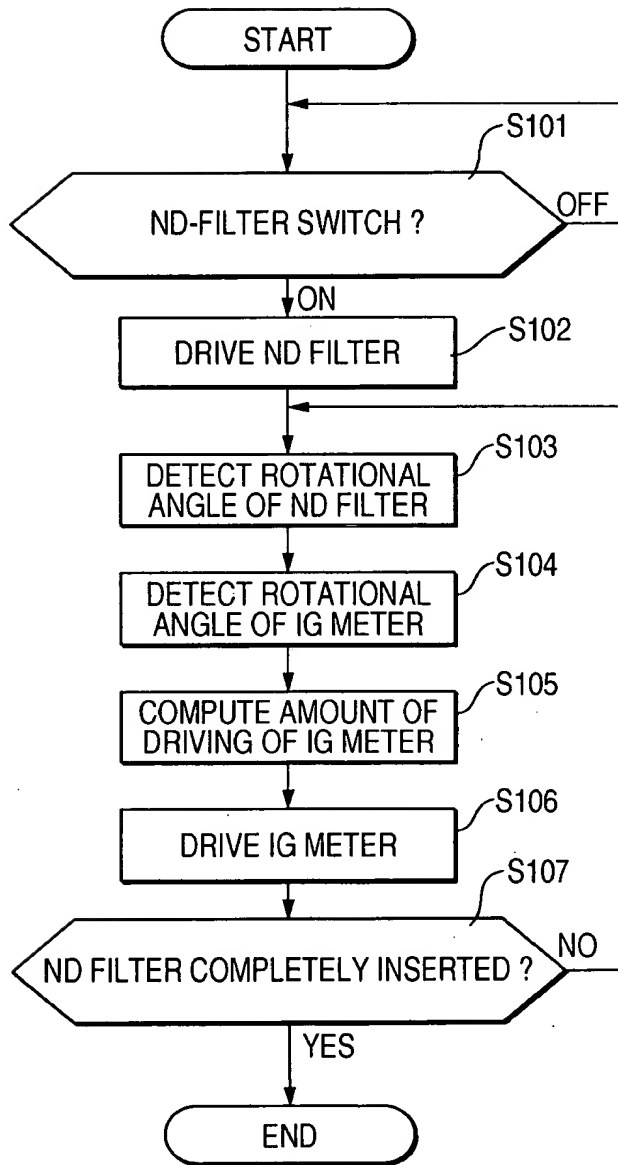


FIG. 13

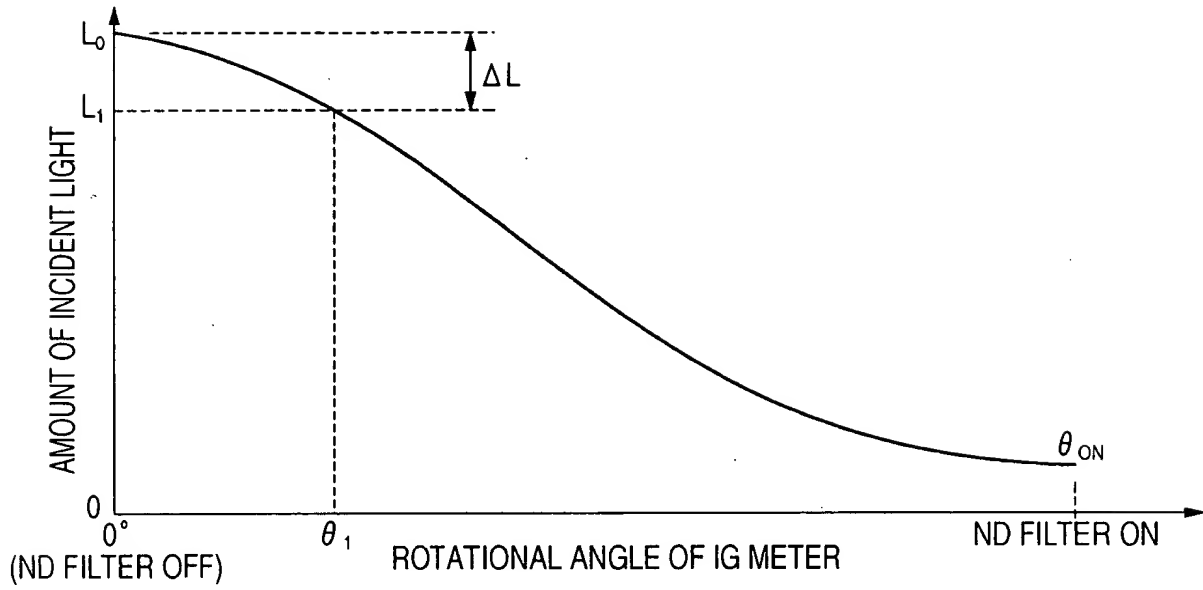


FIG. 14

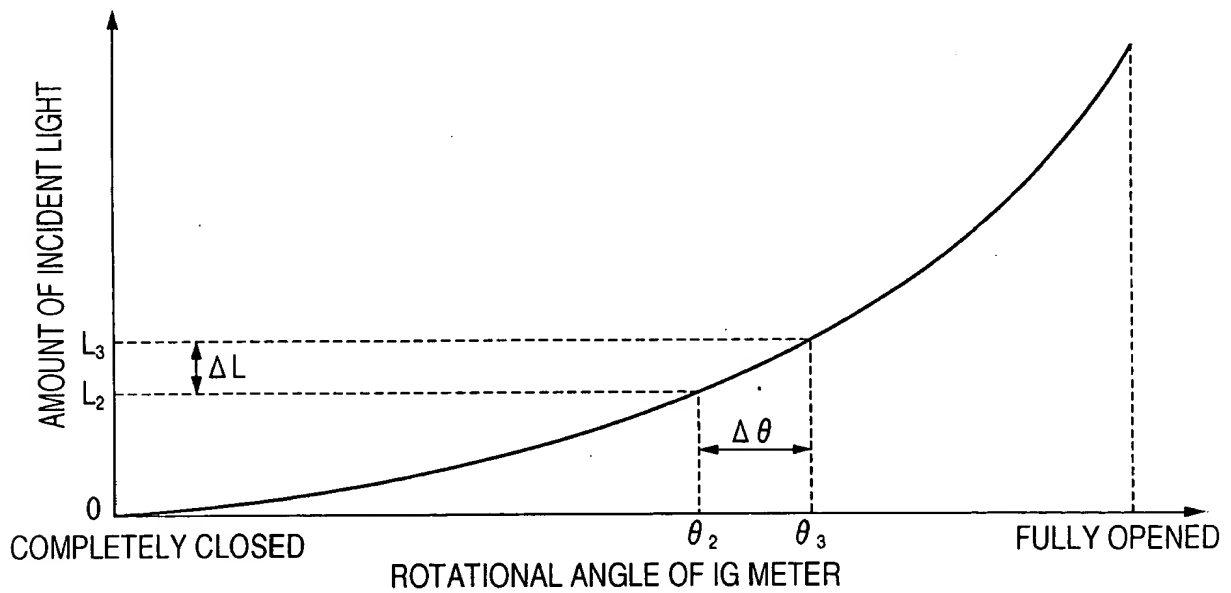


FIG. 15

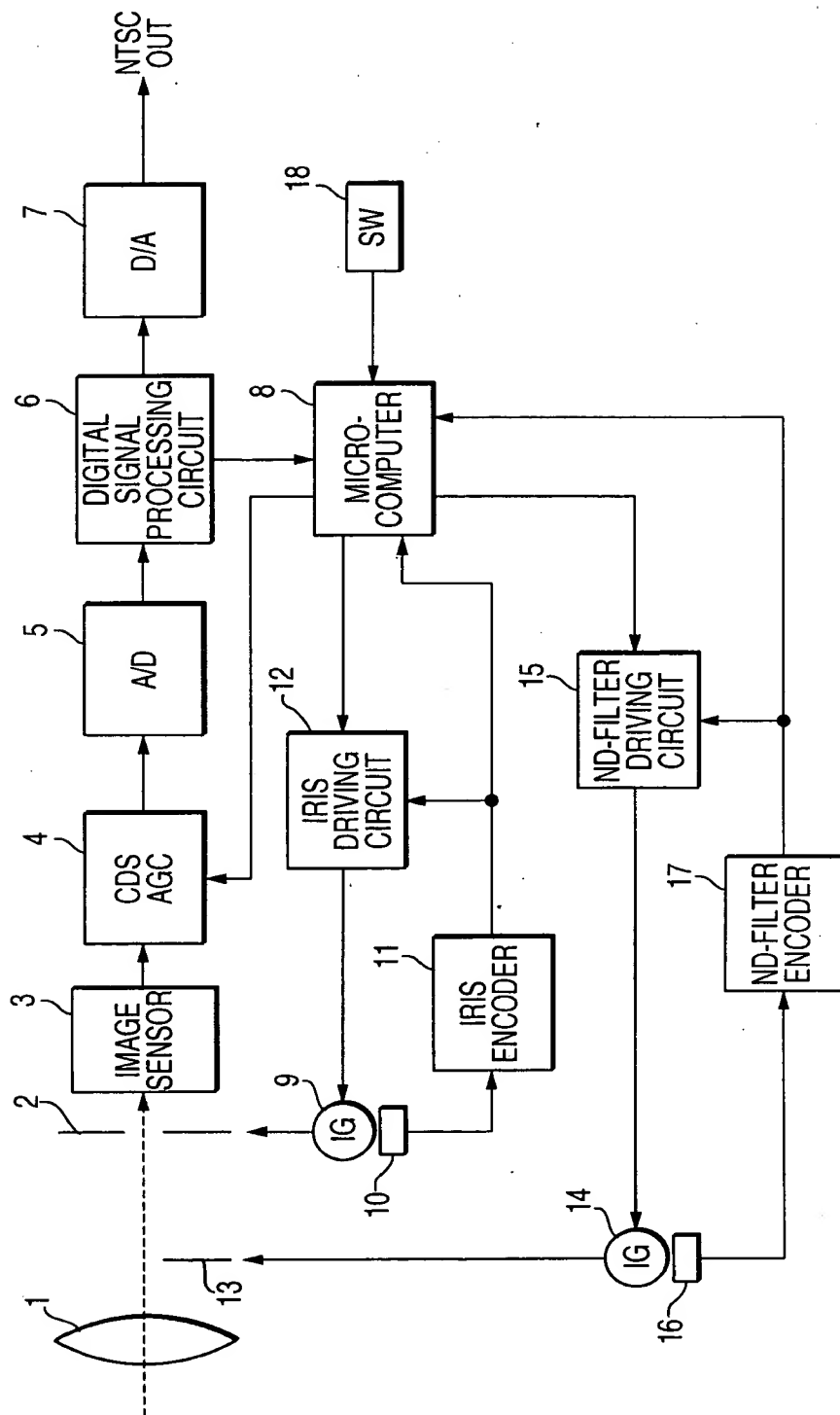


FIG. 16

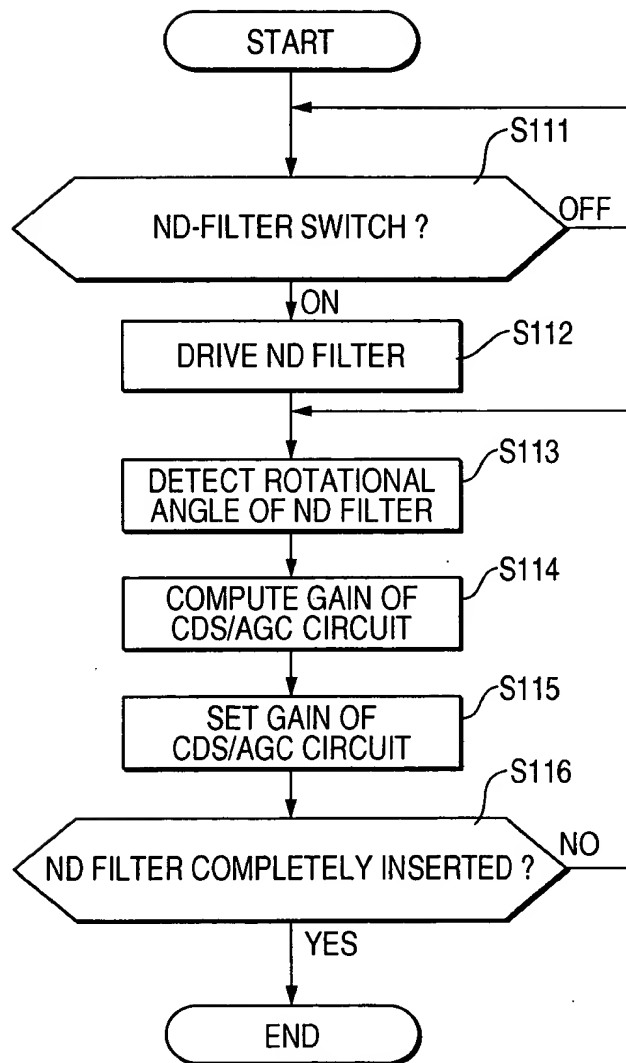


FIG. 17

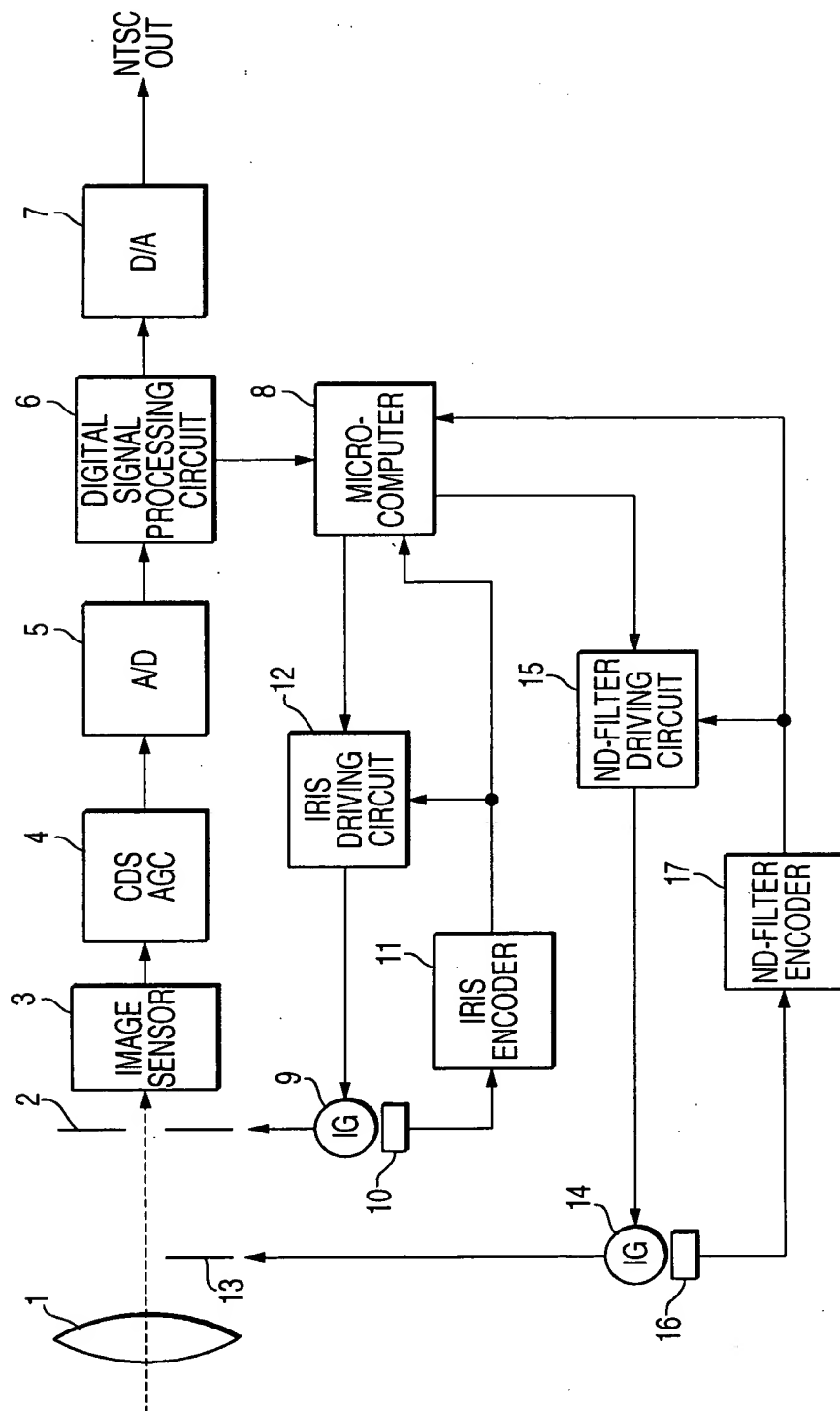


FIG. 18

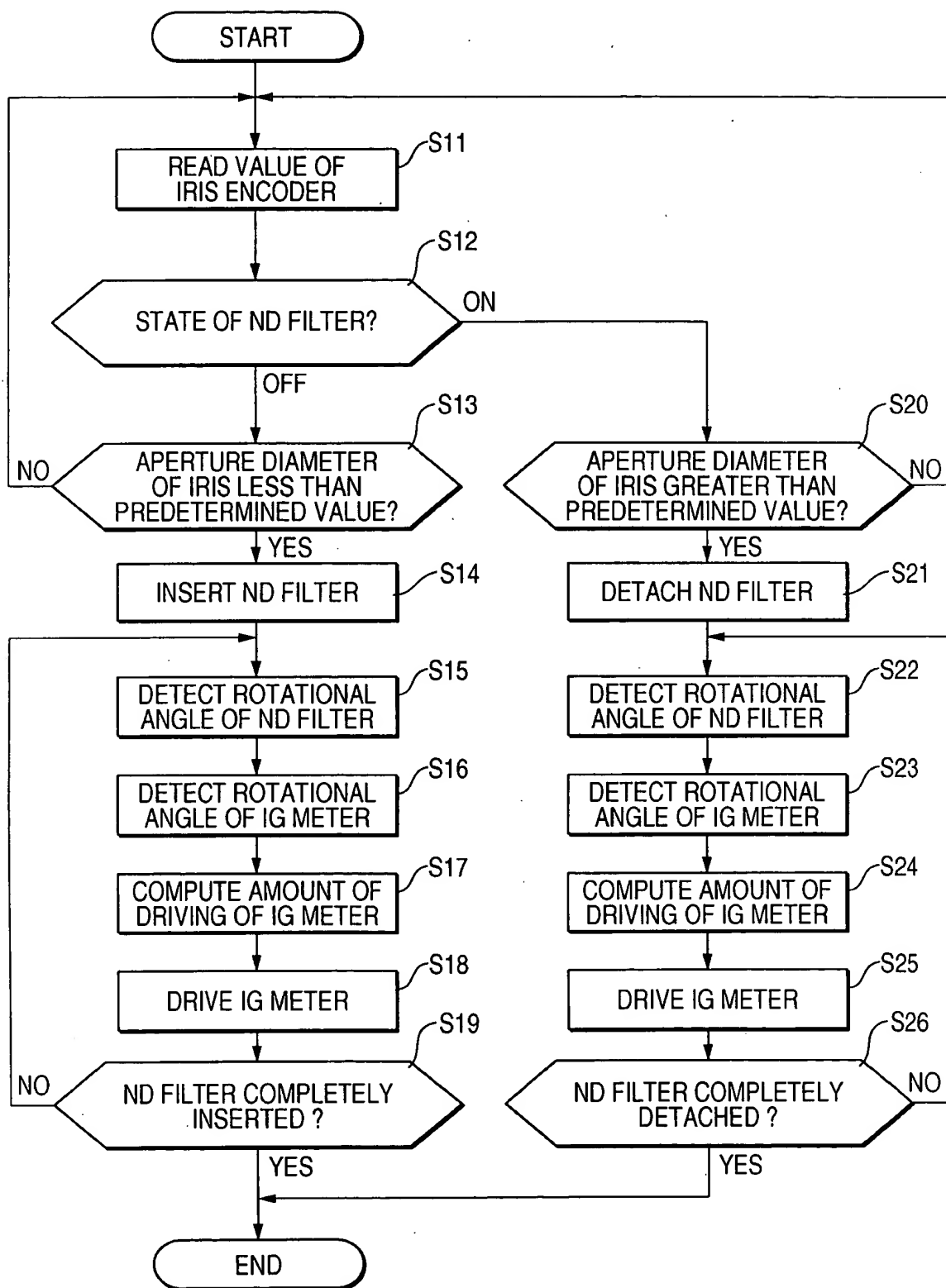


FIG. 19

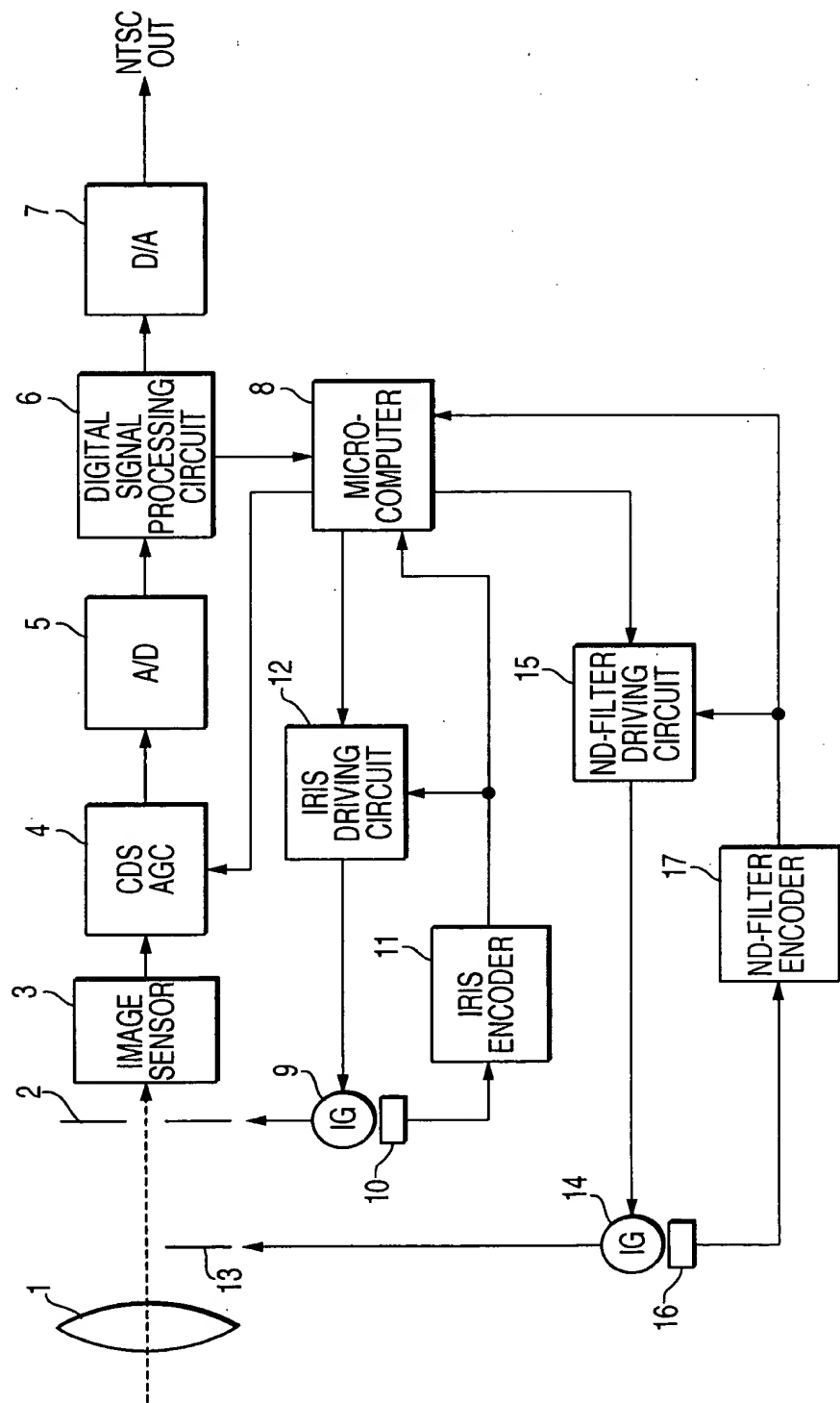


FIG. 20

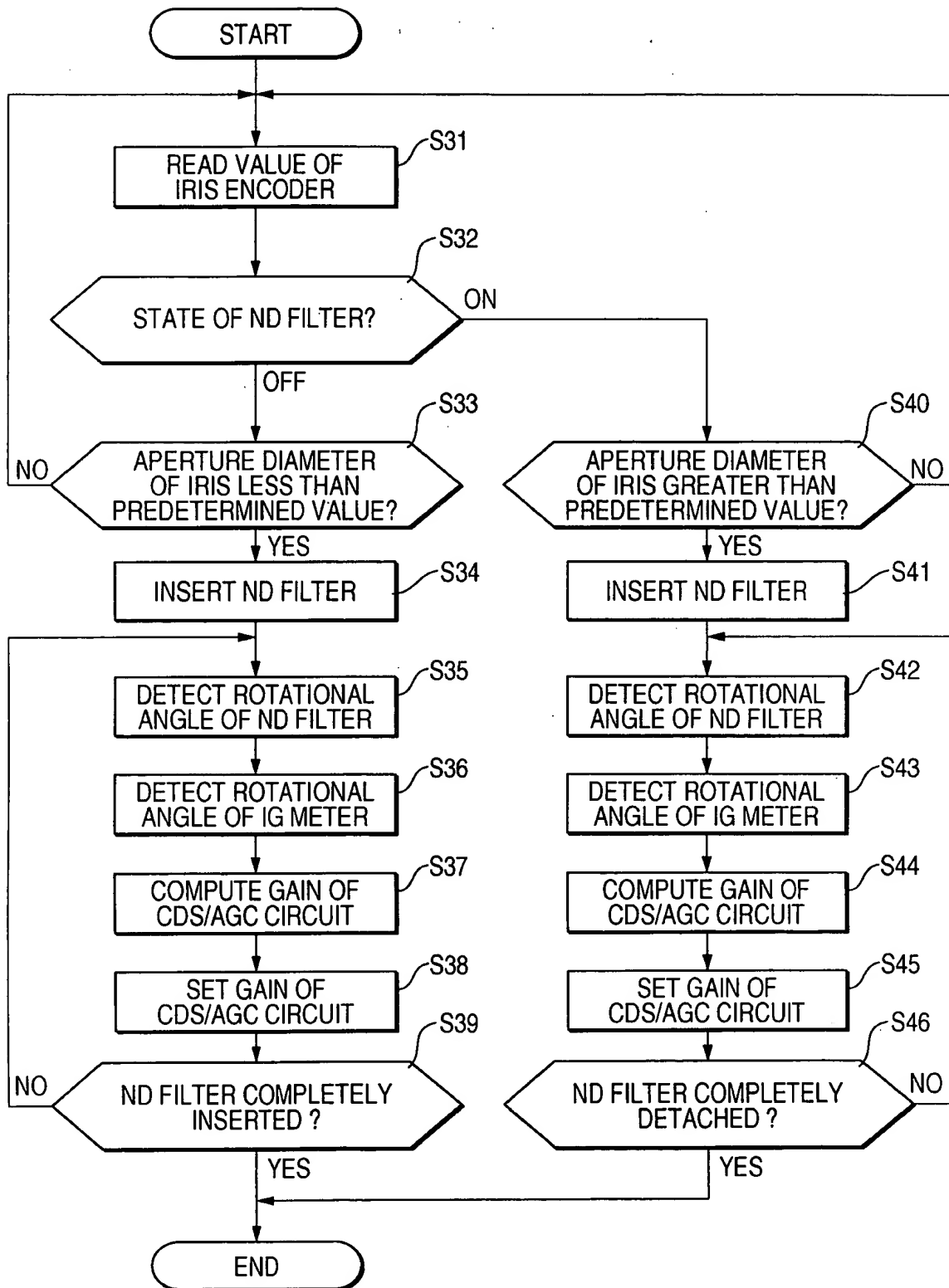


FIG. 21

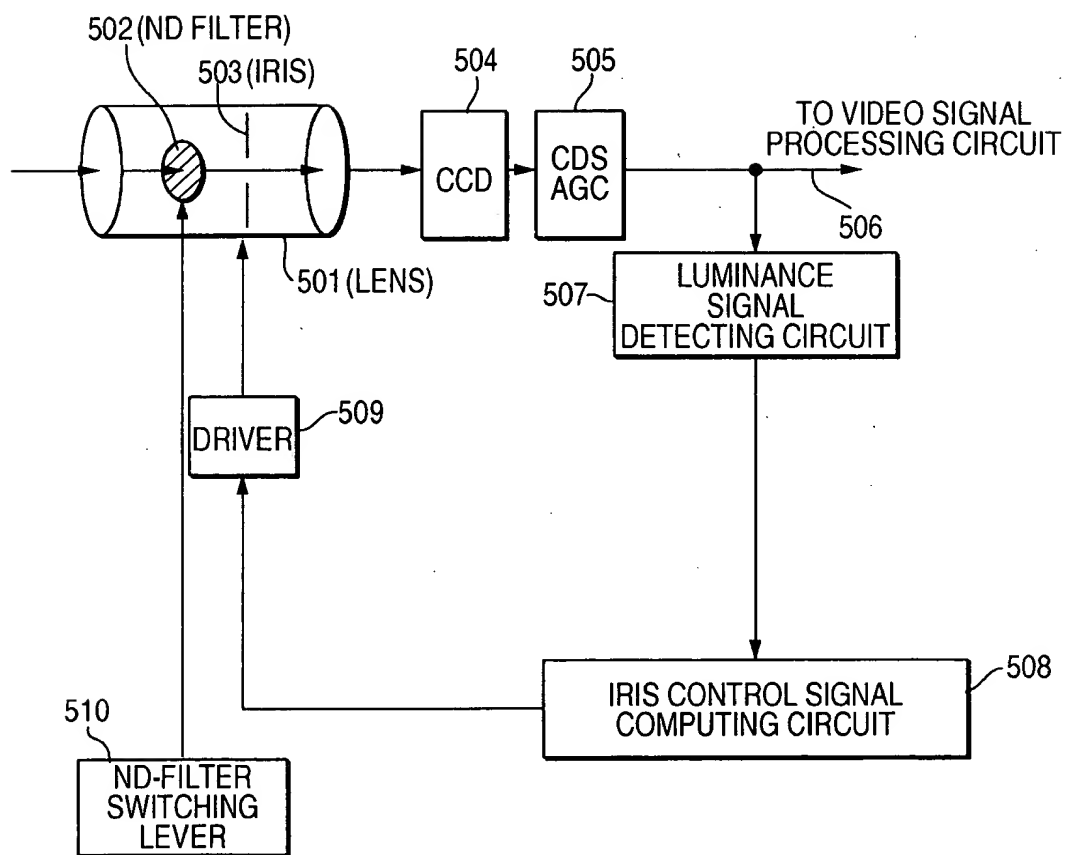


FIG. 22

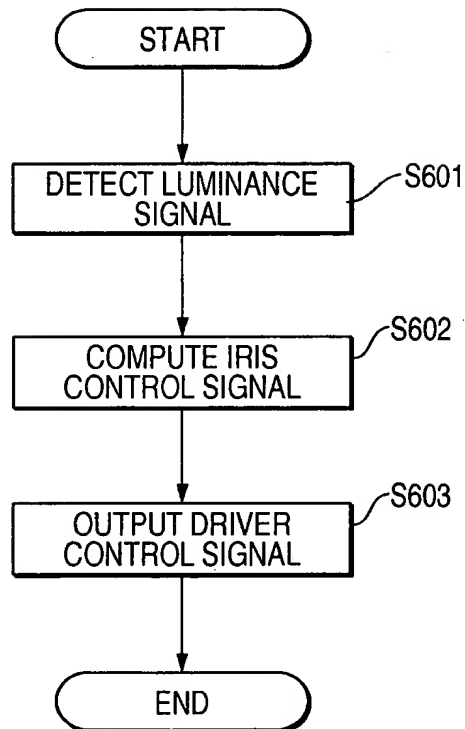


FIG. 23

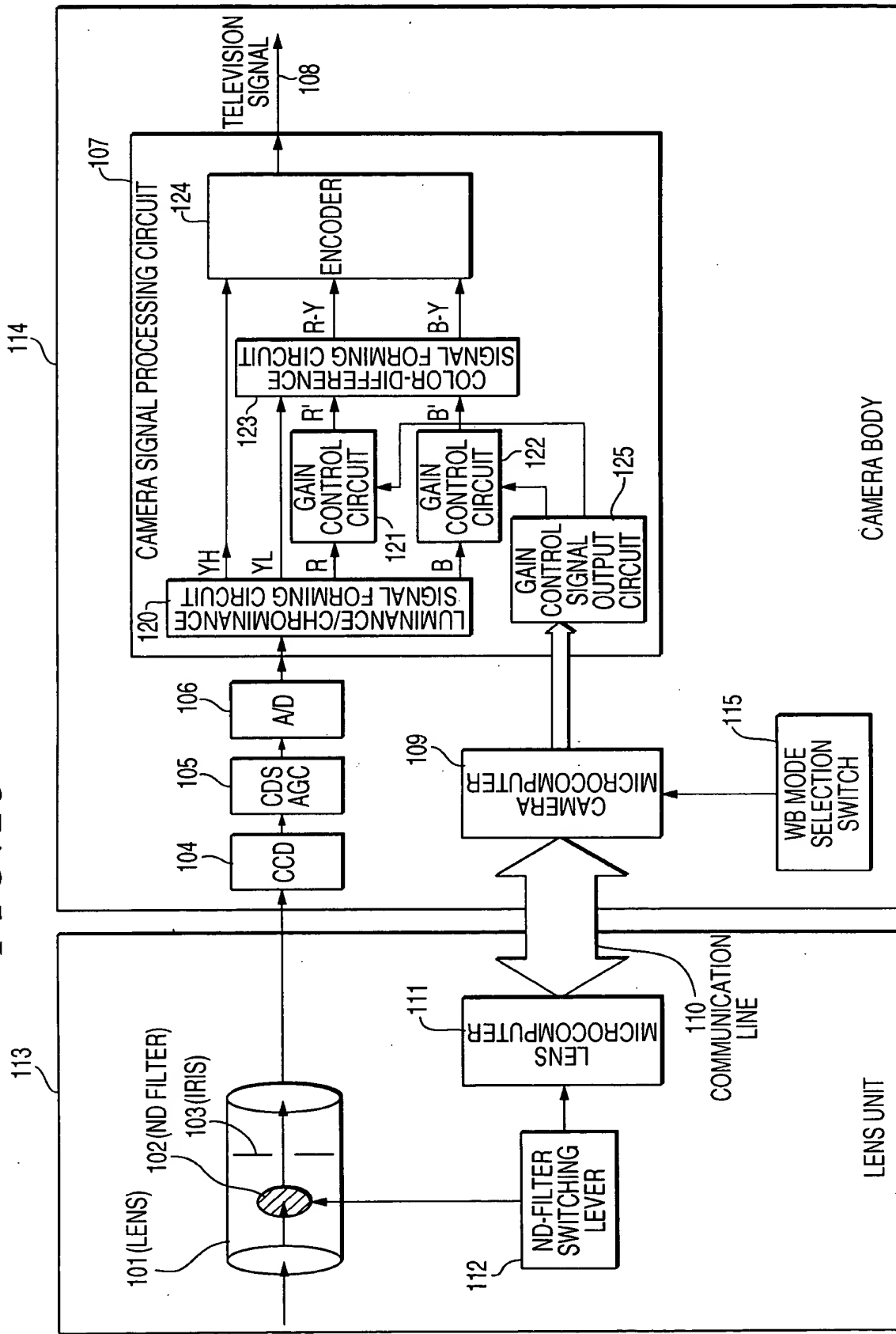


FIG. 24

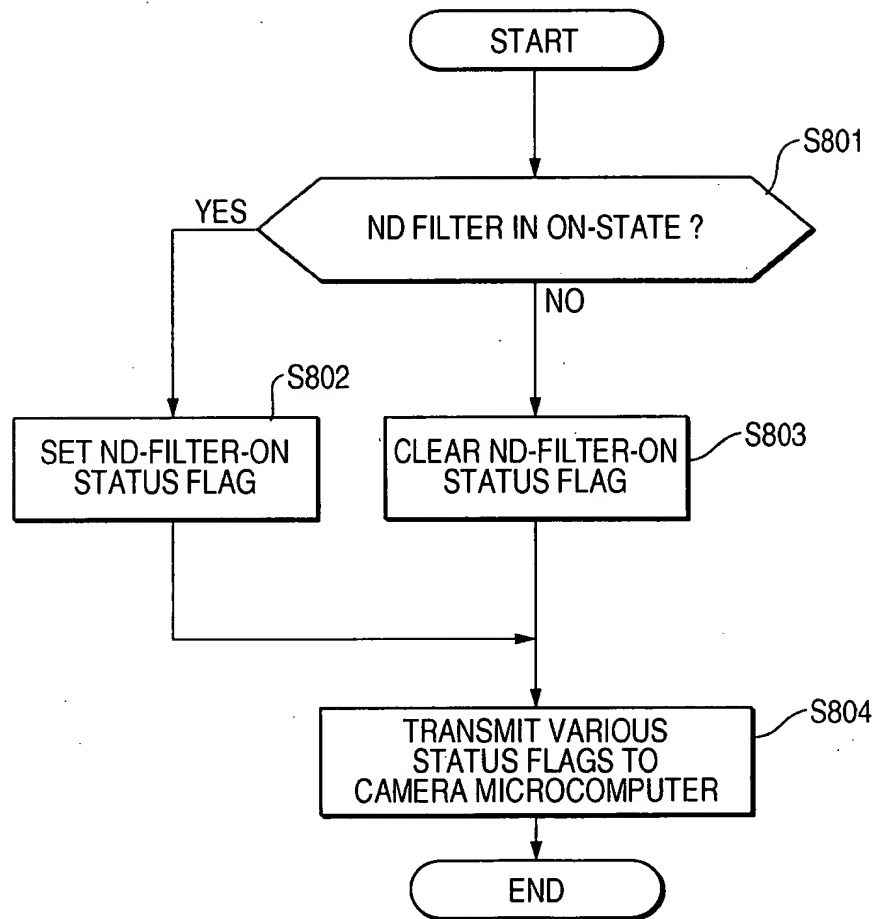


FIG. 25

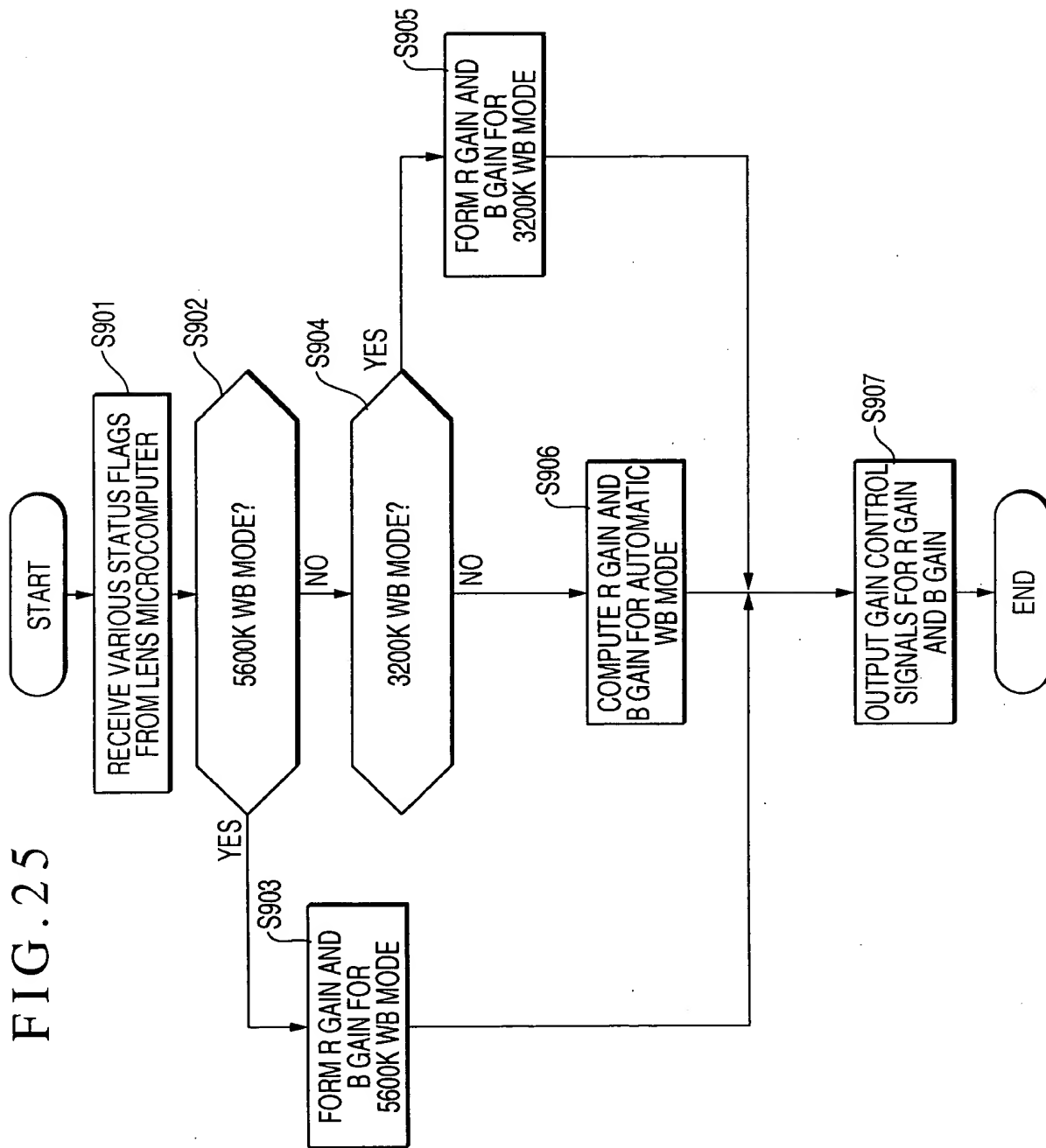


FIG. 26(a)

5600K WB MODE WHEN ND FILTER IS OFF

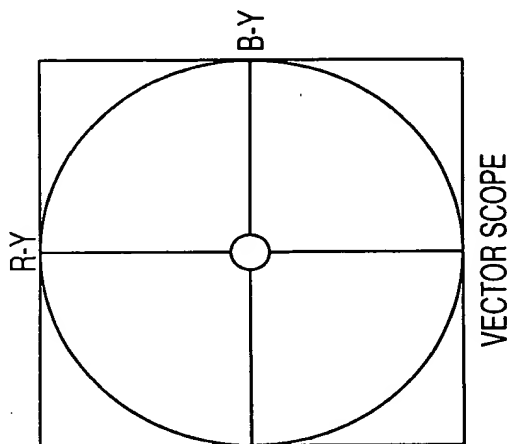


FIG. 26(b)

5600K WB MODE WHEN ND FILTER IS ON
1001 (AMOUNT OF COLOR DEVIATION IN R-Y)

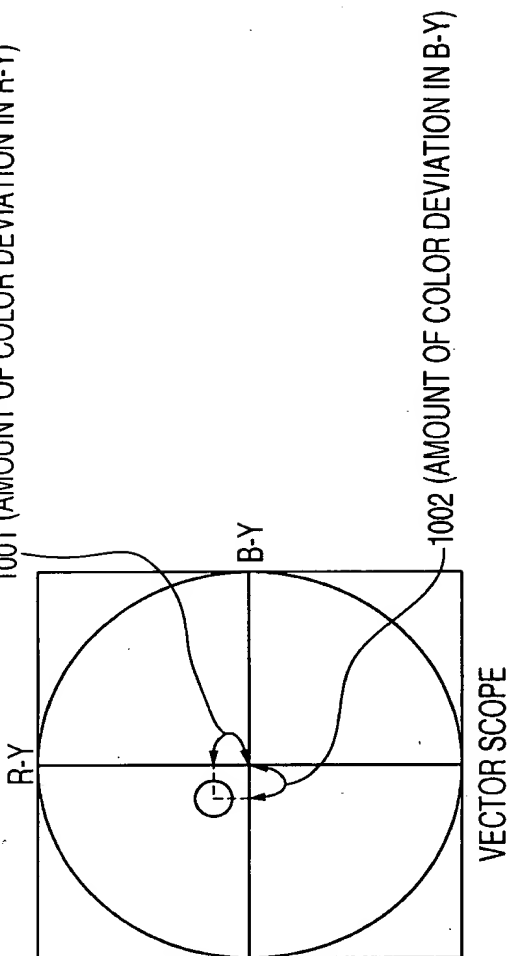


FIG. 26(c)

3200K WB MODE WHEN ND FILTER IS OFF

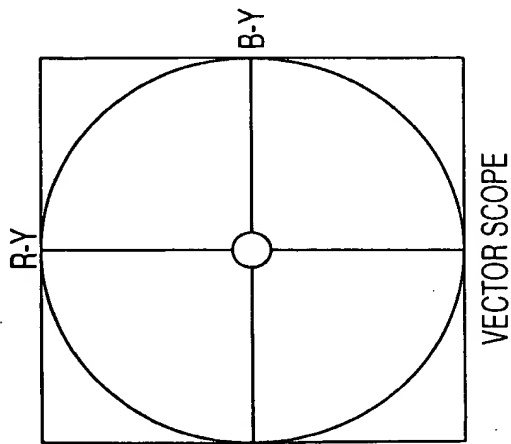


FIG. 26(d)

3200K WB MODE WHEN ND FILTER IS ON
1003 (AMOUNT OF COLOR DEVIATION IN R-Y)

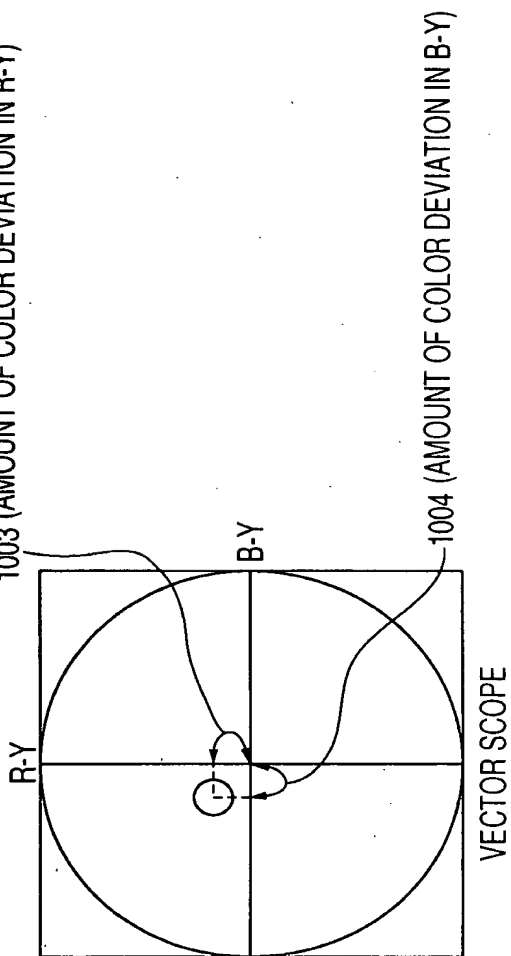


FIG. 27

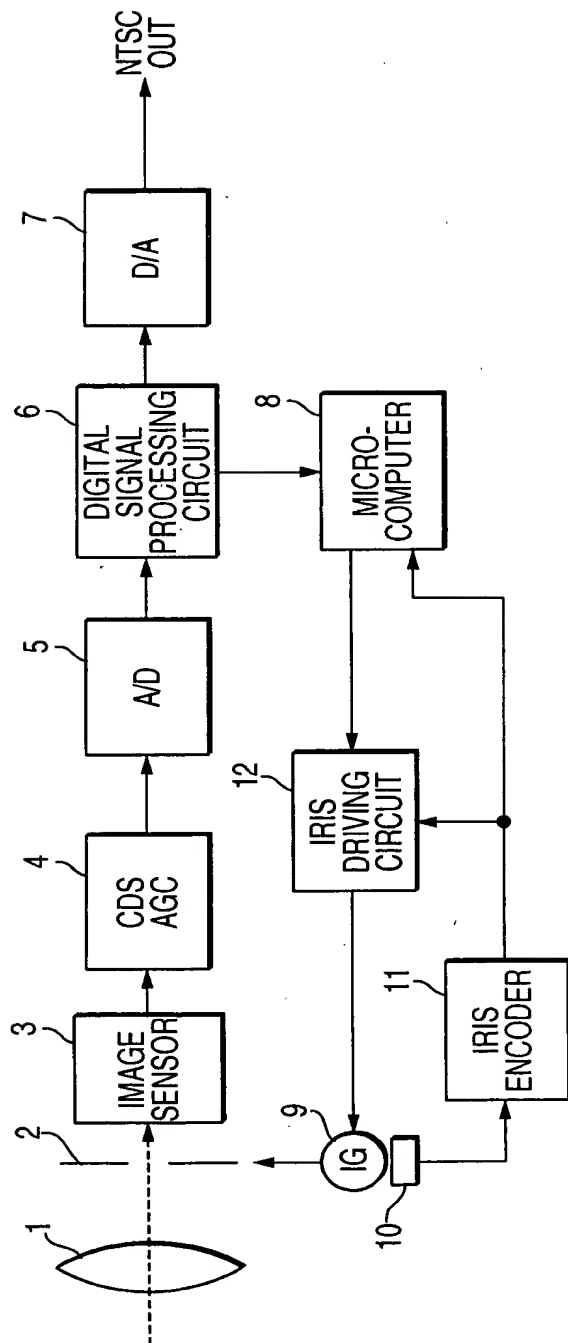


FIG. 28(a)

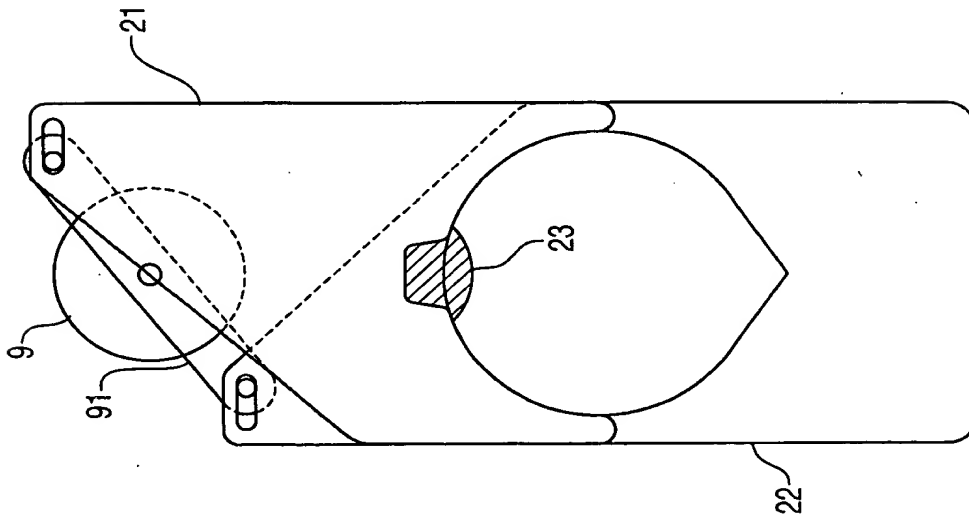


FIG. 28(b)

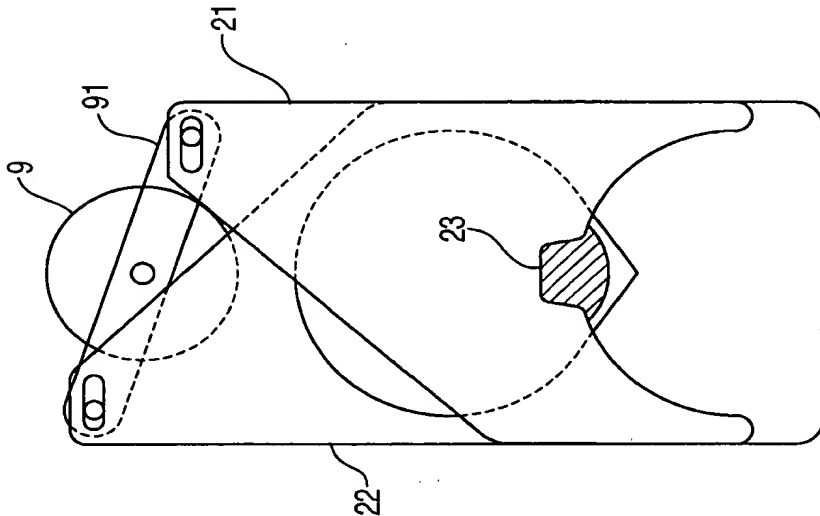


FIG. 28(c)

